

Pb Free Plating Product

HER2002PT thru HER2012PT



20.0 Ampere Heatsink Dual Common Cathode High Efficiency Rectifiers

<p>Features</p> <ul style="list-style-type: none"> ThinkiSemi latest&matured process FRD/FRED Low forward voltage drop High current capability Low reverse leakage current High surge current capability <p>Application</p> <ul style="list-style-type: none"> Automotive Inverters and Solar Inverters Car Audio Amplifiers and Sound Device Systems Plating Power Supply, Motor Control, UPS and SMPS etc. <p>Mechanical Data</p> <ul style="list-style-type: none"> Case: Heatsink TO-247AD/TO-3P Package Outline Epoxy: UL 94V-0 rate flame retardant Terminals: Solderable per MIL-STD-202 method 208 Polarity: As marked on diode body Mounting position: Any Weight: 6.0 gram approximately 	<p>TO-247AD/TO-3P Unit:inch(mm)</p> <p>① → ② ③ ← Case</p> <p>① ← ② ③ ← Case</p> <p>① → ② ③ ← Case</p> <p>① ← ② ③ ← Case</p> <p>Positive Common Cathode Suffix "PT" Negative Common Anode Suffix "PA" Doubler Tandem Polarity Suffix "PR" Series Tandem Polarity Suffix "PL"</p>
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MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25°C ambient temperature unless otherwise specified.
Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

PARAMETER	SYMBOL	HER2002PT	HER2003PT HER2004PT	HER2006PT	HER2008PT	HER2010PT	HER2012PT	UNIT
Maximum Recurrent Peak Reverse Voltage	VRRM	200	400	600	800	1000	1200	V
Maximum RMS Voltage	VRMS	140	280	420	560	700	840	V
Maximum DC Blocking Voltage	VDC	200	400	600	800	1000	1200	V
Maximum Average Forward Rectified Current TC=125°C (Total Device 2x10.0A=20.0A)	IF(AV)	20.0						A
Peak Forward Surge Current, 8.3ms single Half sine-wave superimposed on rated load (JEDEC method)(Per Diode/Per Leg)	IFSM	200						A
Maximum Instantaneous Forward Voltage @10.0A(Per Diode/Per Leg)	VF (Typical)	0.85-1.00	1.00-1.30	1.30-1.70	1.30-1.70			V
Maximum DC Reverse Current @TJ=25°C At Rated DC Blocking Voltage @TJ=125°C	IR	1.0 100						µA µA
Maximum Reverse Recovery Time (Note1)	Trr	35-50			50-75			nS
Typical Junction Capacitance (Note 2)	CJ	100						pF
Typical Thermal Resistance (Note 3)	RθJC	0.75						°C/W
Operating Junction and Storage Temperature Range	TJ,TSTG	-55 to +175						°C

Note:(1)Reverse recovery test conditions IF = 0.5A, IR = 1.0A, Irr = 0.25A.
Note:(2)Measured at 1.0 MHz and applied reverse voltage of 4.0 Volts DC.
Note:(3)Thermal Resistance junction to case.

FIG.1 - FORWARD CURRENT DERATING CURVE

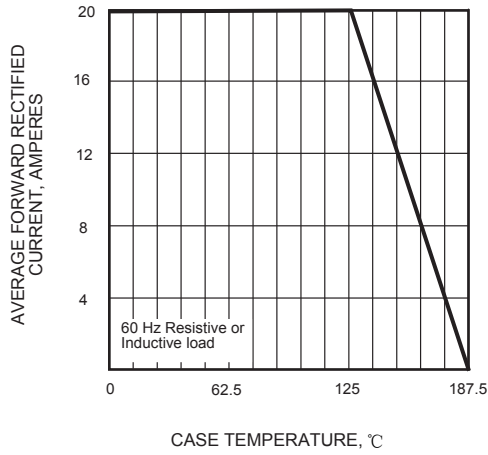


FIG.2 - MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

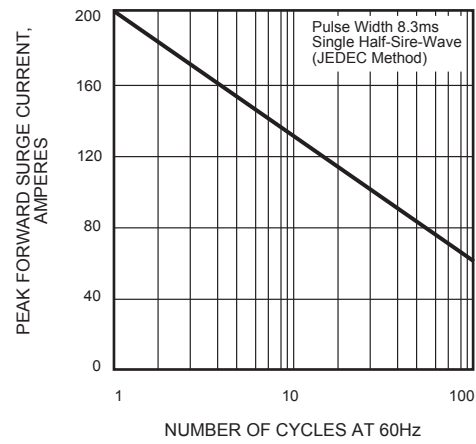


FIG.3 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

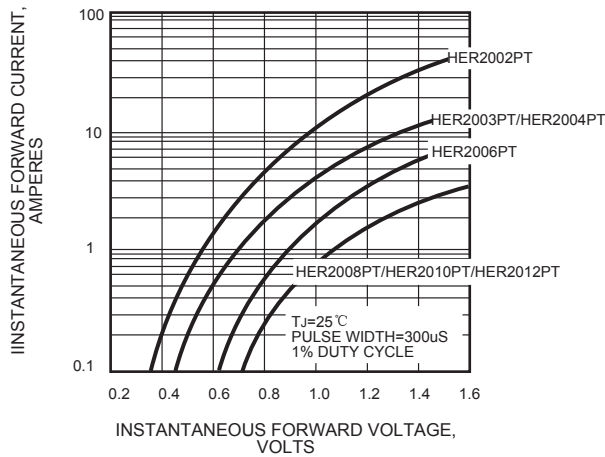


FIG.4 - TYPICAL REVERSE CHARACTERISTICS

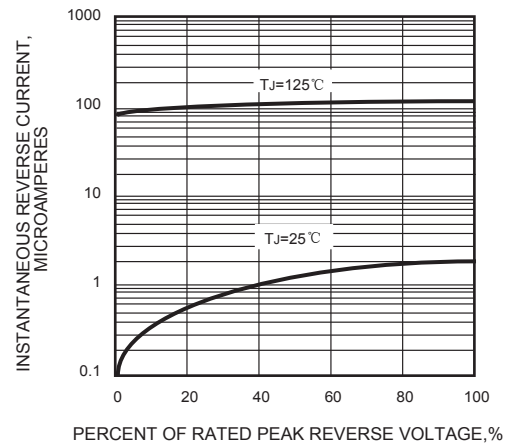


FIG.5 - TYPICAL JUNCTION CAPACITANCE

